

TITLE: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUIT WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
INVENTORS NAME: Leonard Forbes
DOCKET NO.: 1303.029US1

1/17

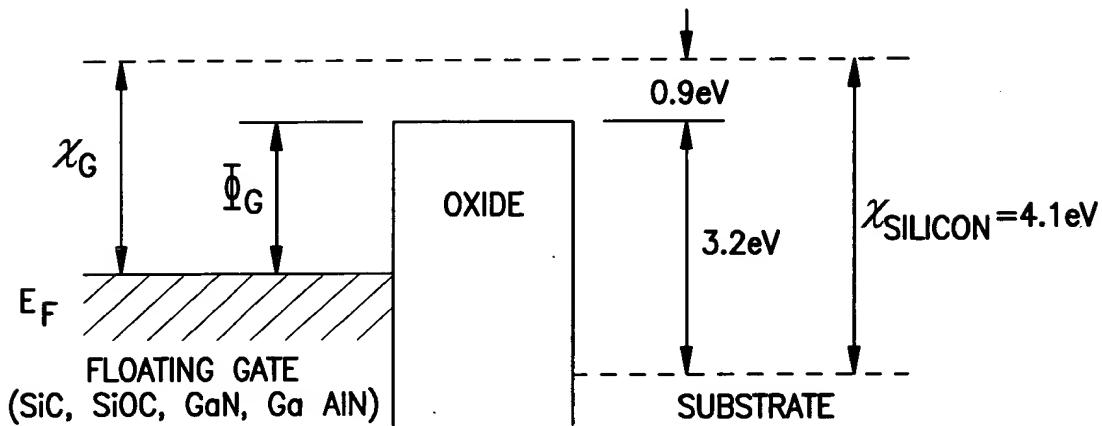


FIG. 1A
(PRIOR ART)

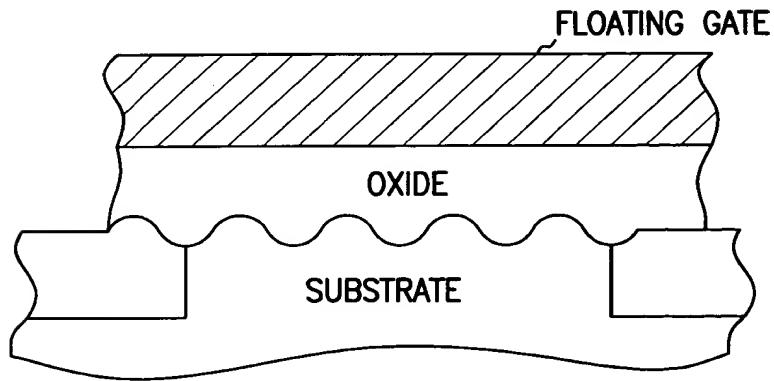


FIG. 1B
(PRIOR ART)

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DOCKET NO.: 1303.029US1

2/17

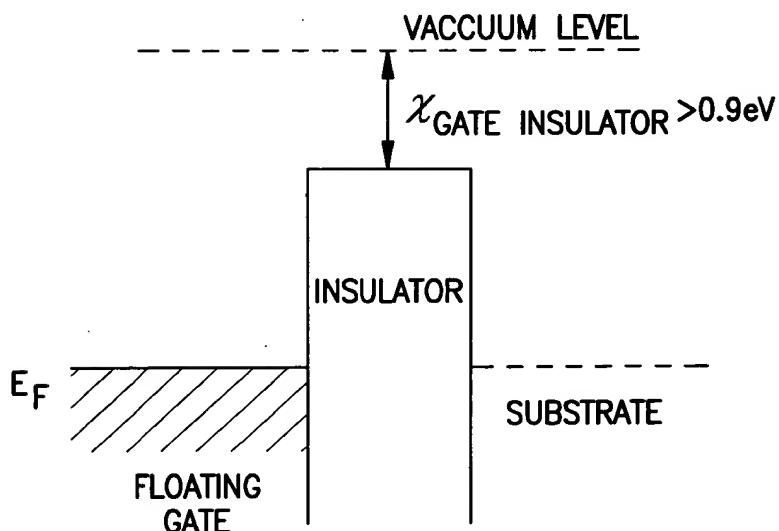


FIG. 1C
(PRIOR ART)

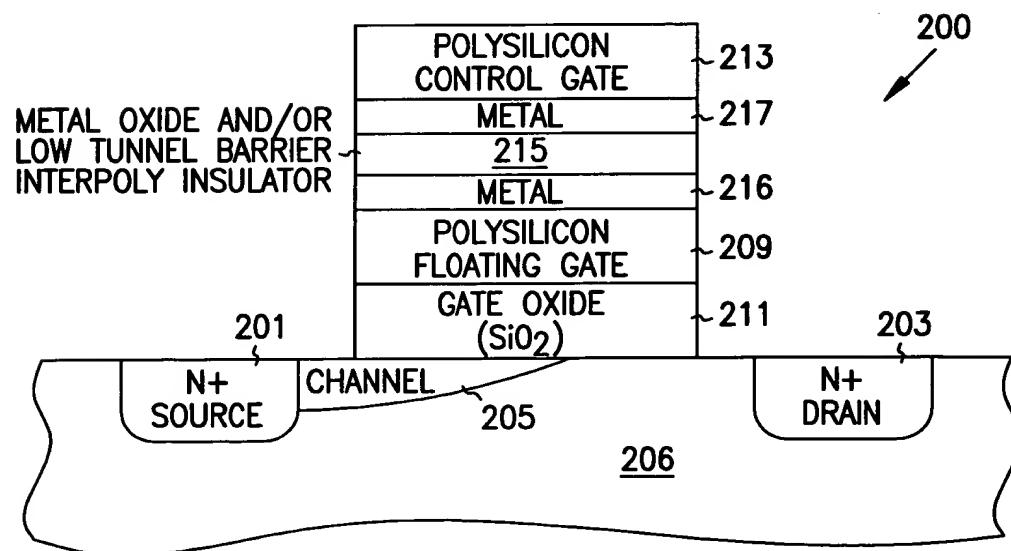


FIG. 2

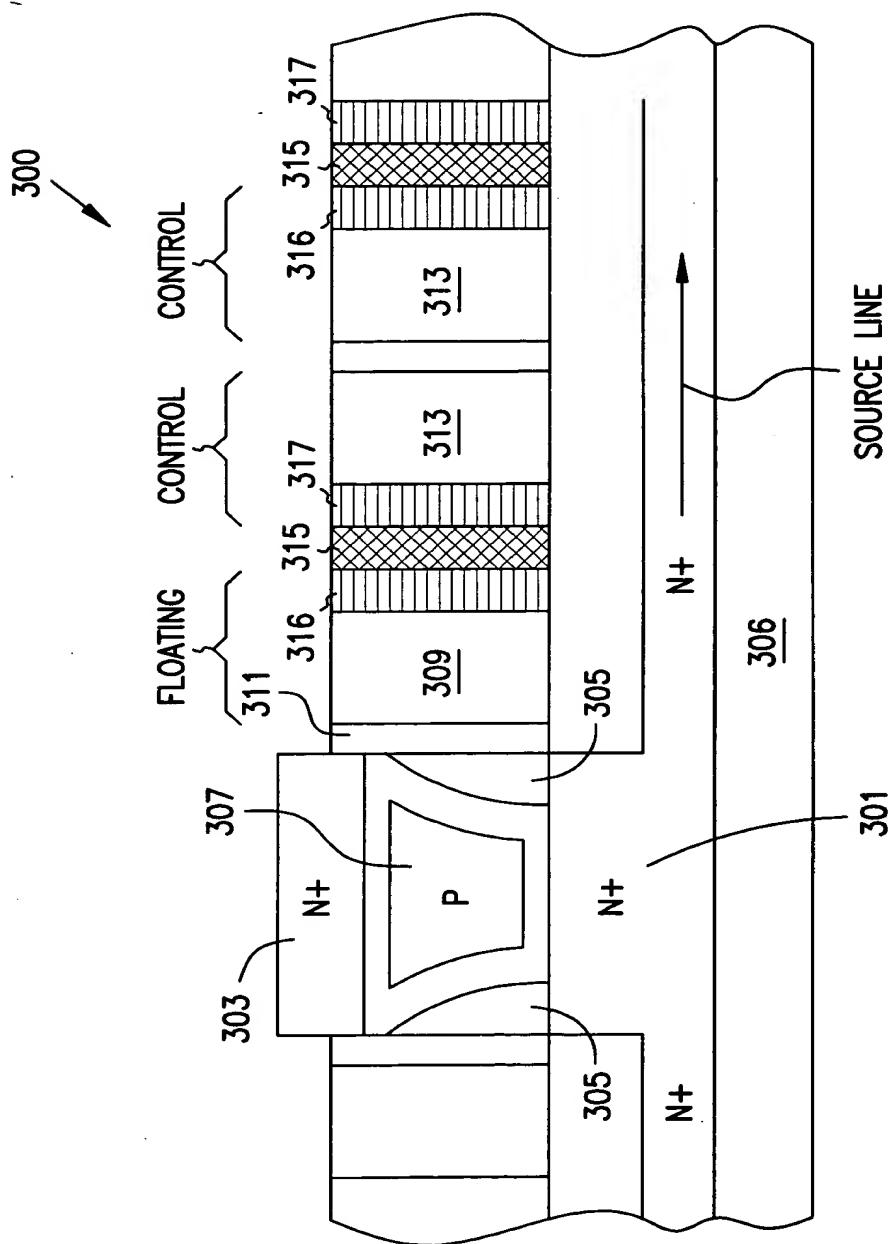


FIG. 3

4/17

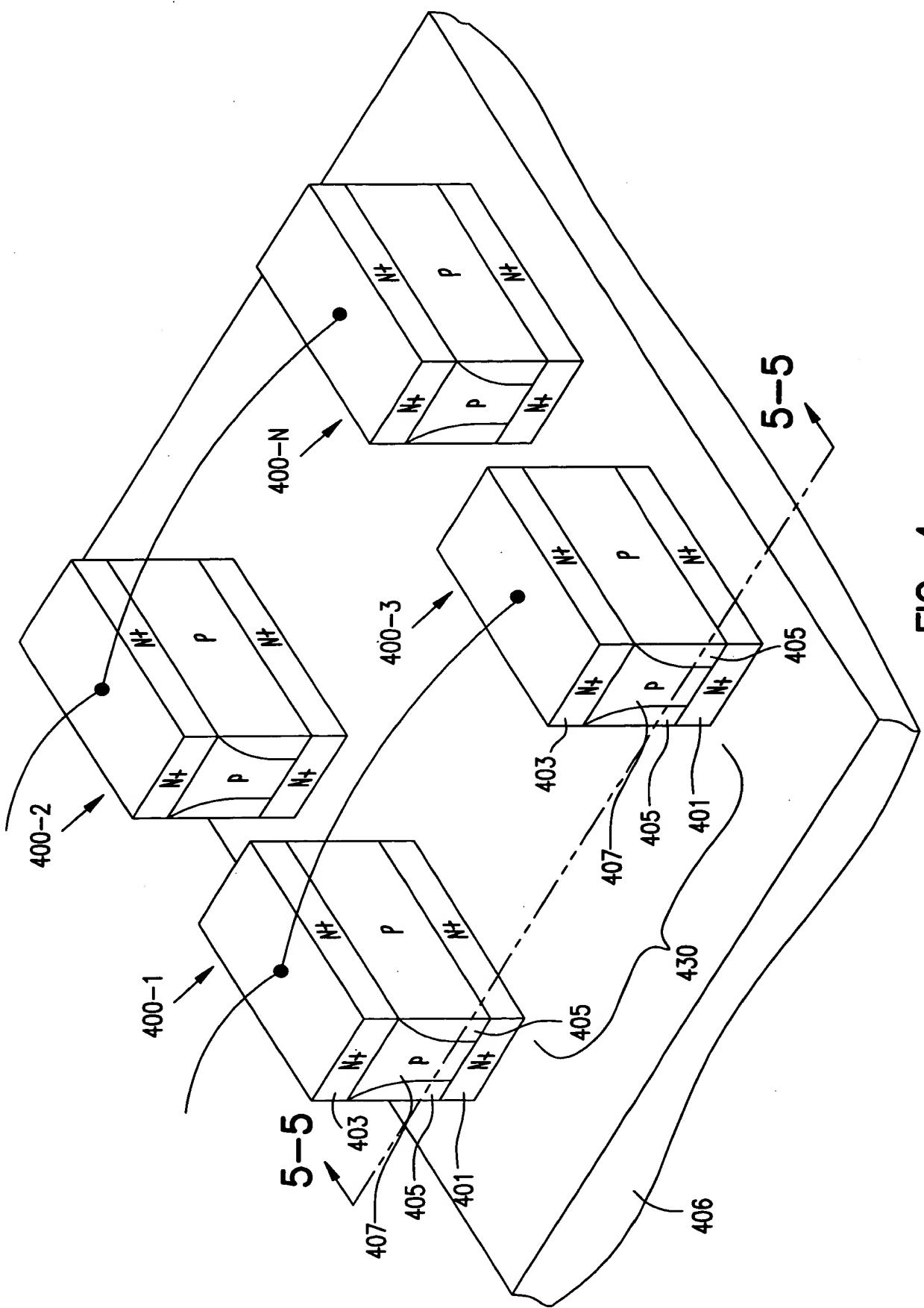


FIG. 4

TITLE: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUIT WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
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5/17

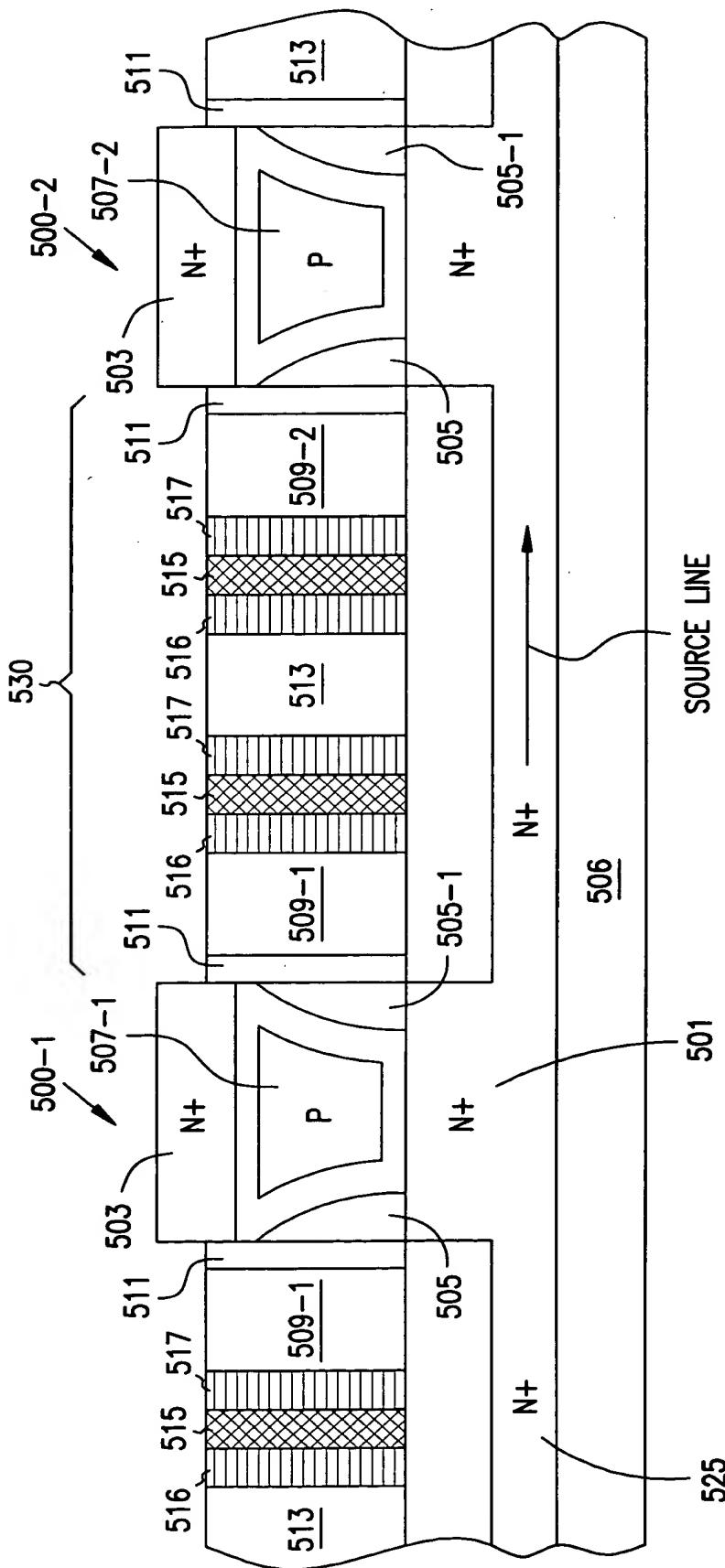


FIG. 5A

7002800-1055500

TITLE: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUIT WITH LOW TUNNEL
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INVENTORS NAME: Leonard Forbes
DOCKET NO.: 1303.029US1

6/17

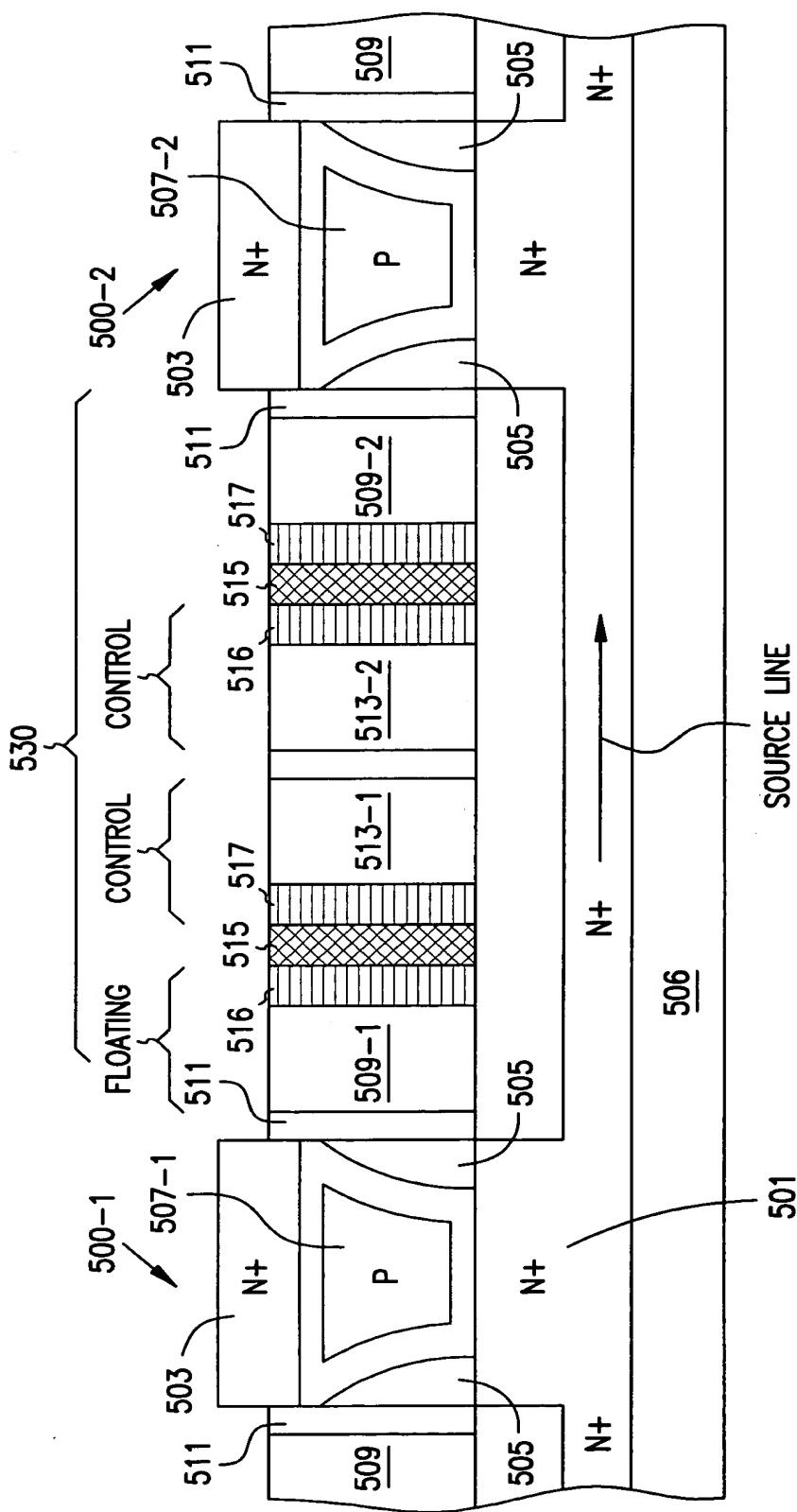


FIG. 5B

TITLE: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUIT WITH LOW TUNNEL
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DOCKET NO.: 1303.029US1

7/17

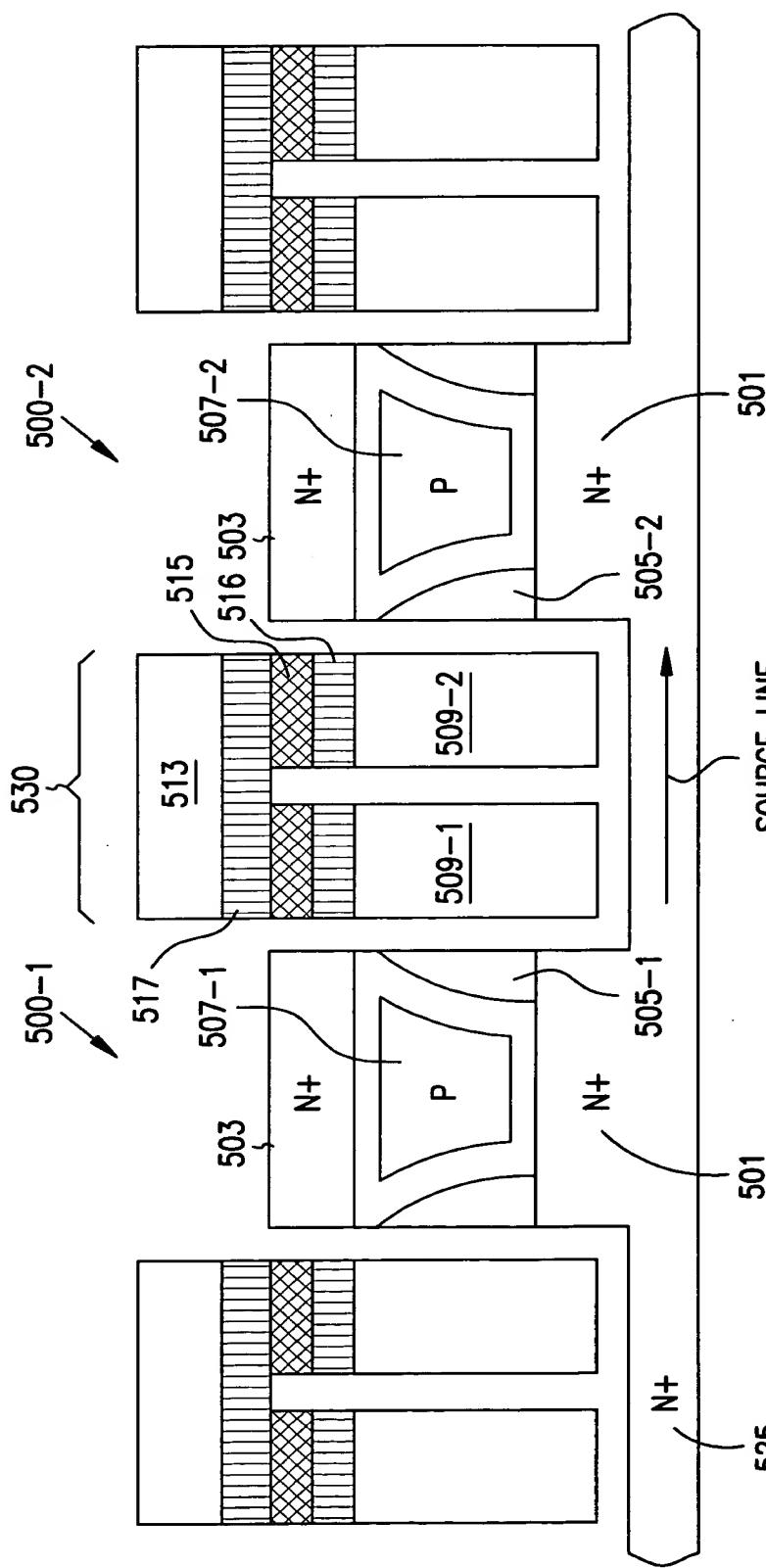


FIG. 5C

TITLE: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUIT WITH LOW TUNNEL
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8/17

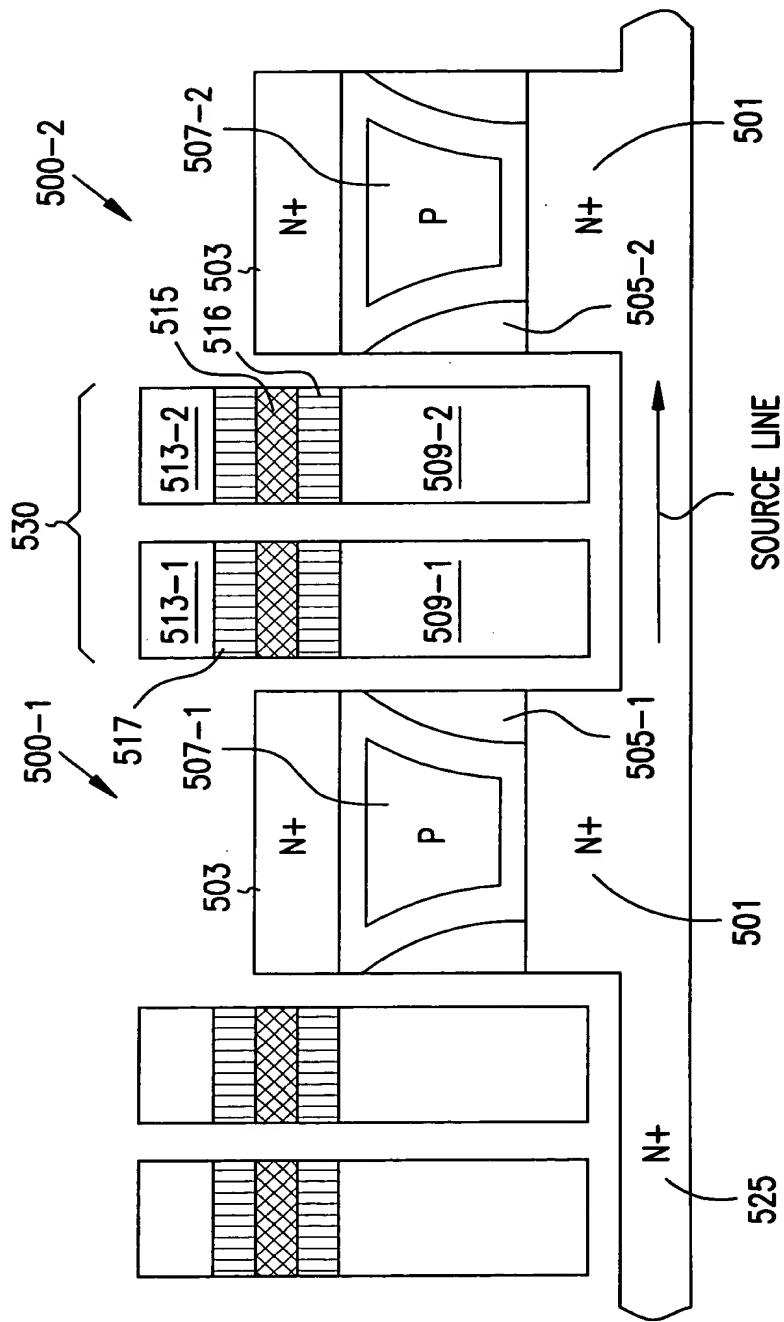


FIG. 5D

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9/17

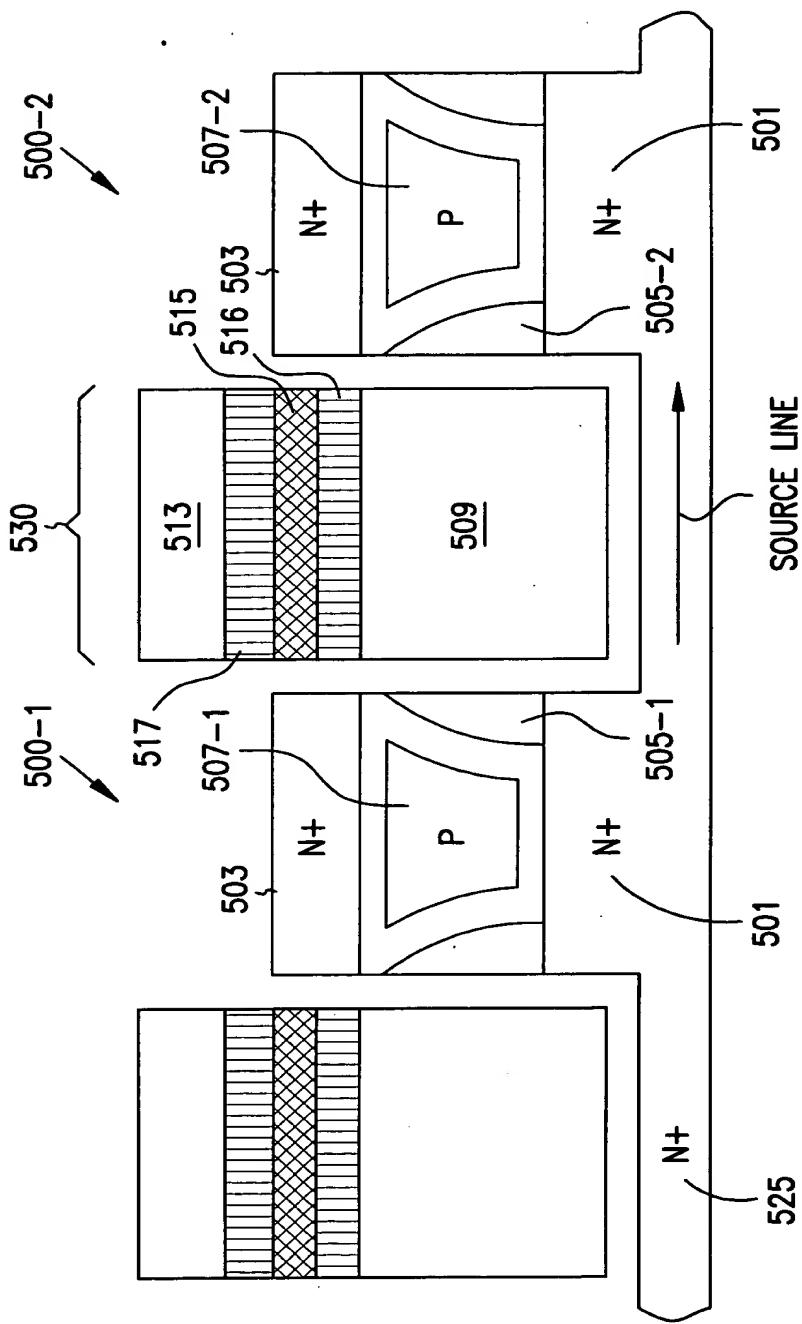


FIG. 5E

10/17

"FIGURE 6D" DESIGN 60

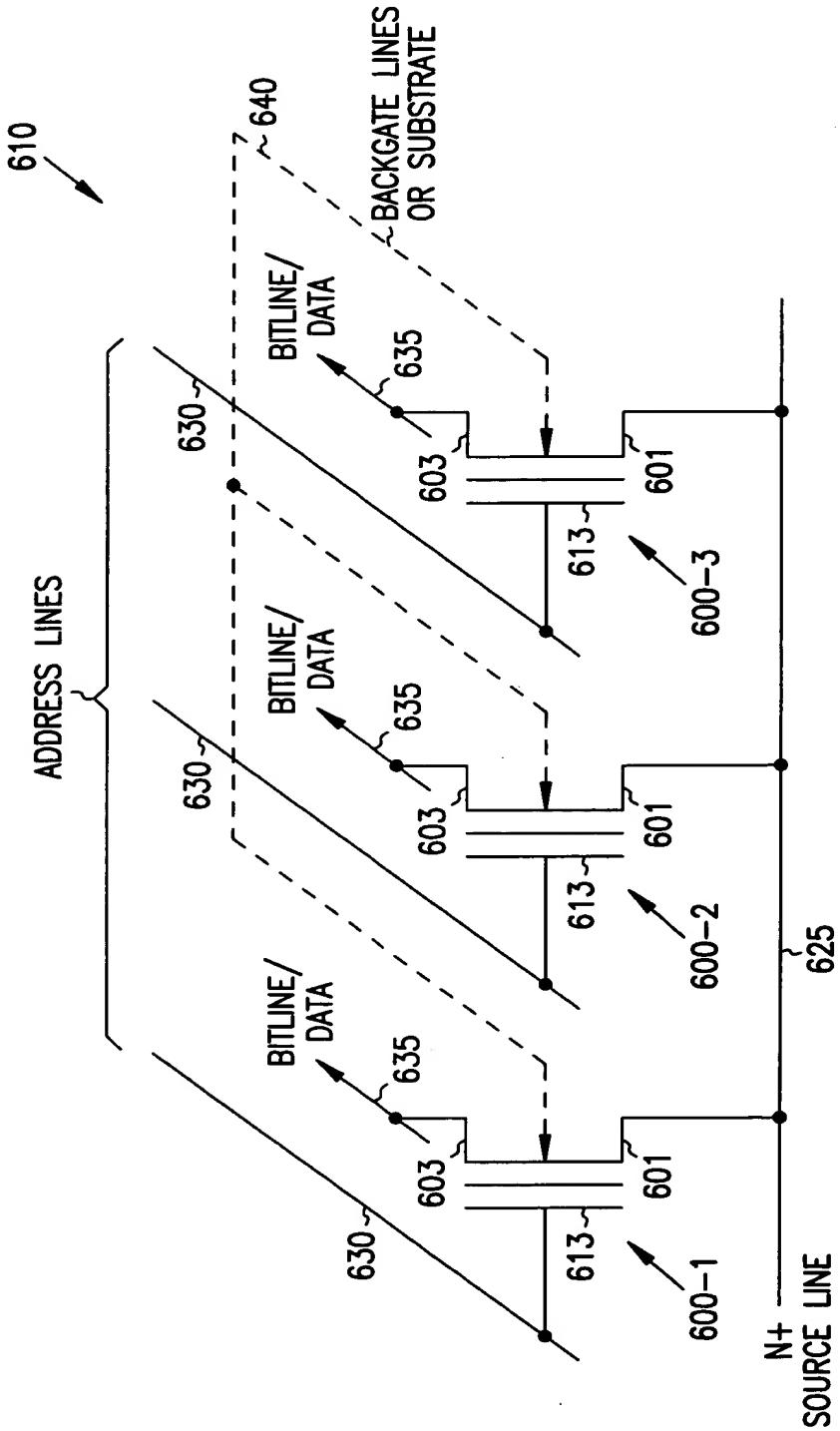


FIG. 6A

TITLE: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUIT WITH LOW TUNNEL
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DOCKET NO.: 1303.029US1

11/17

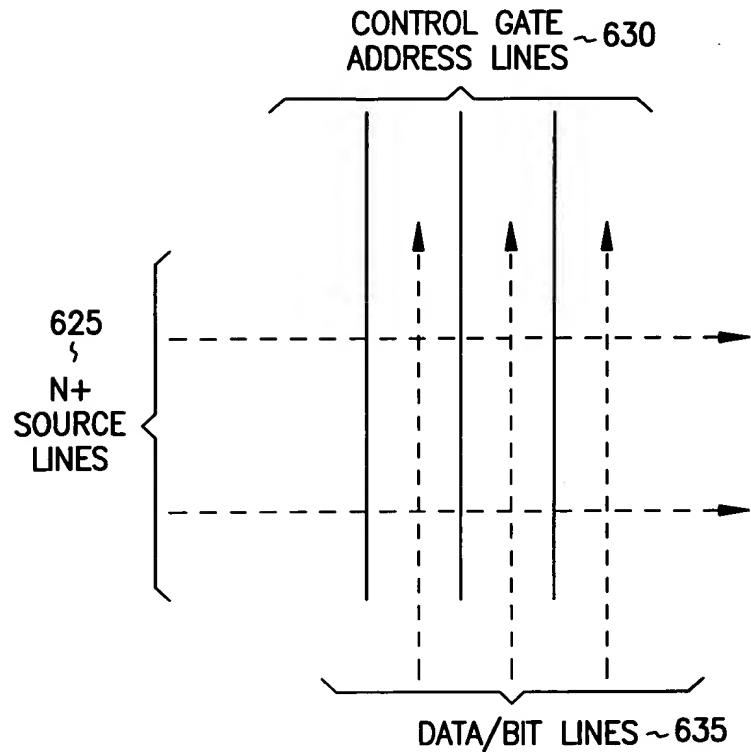


FIG. 6B

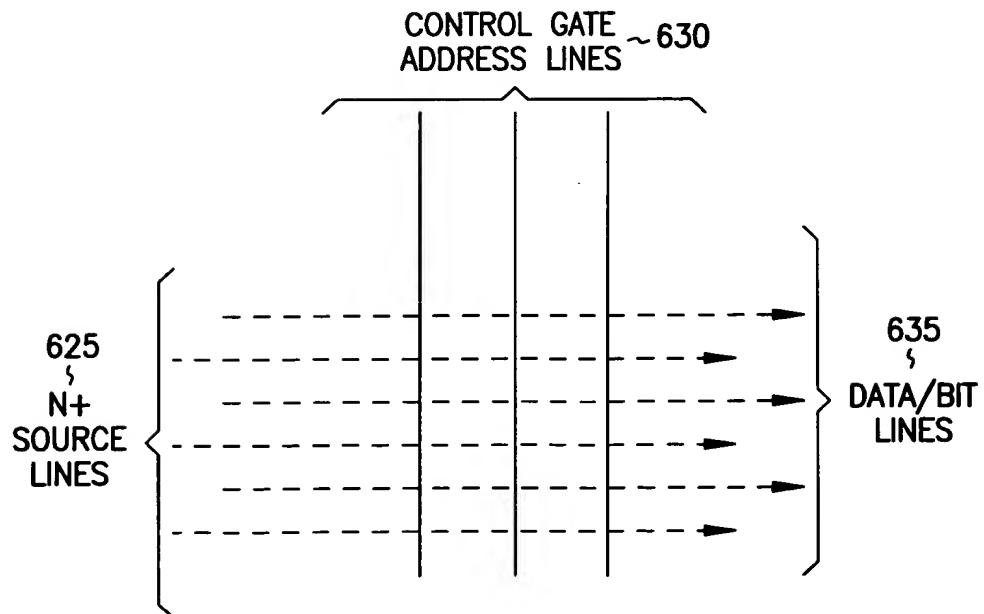


FIG. 6C

FIG. 6B - 00544660

12/17

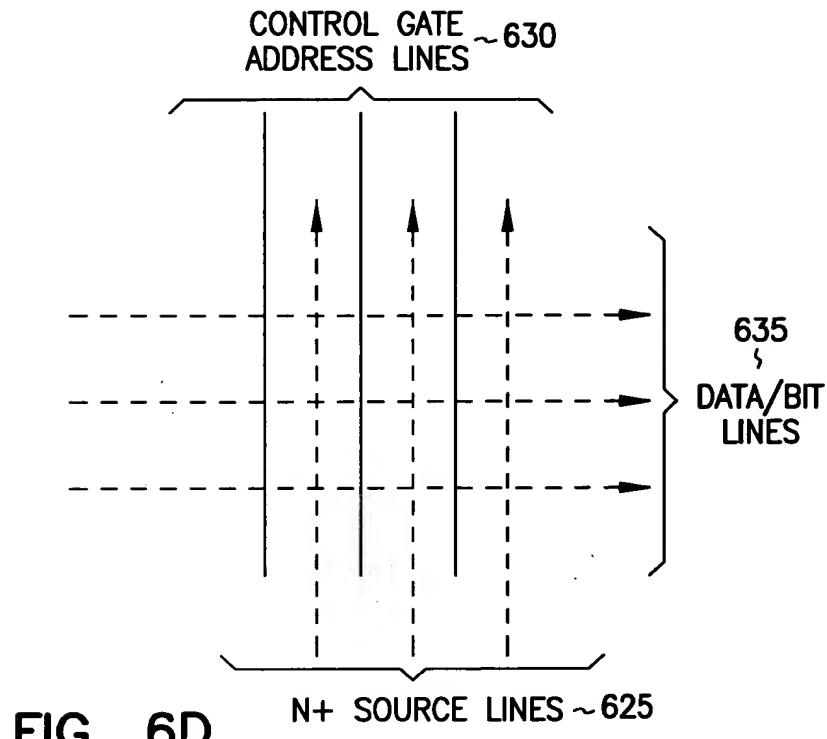


FIG. 6D

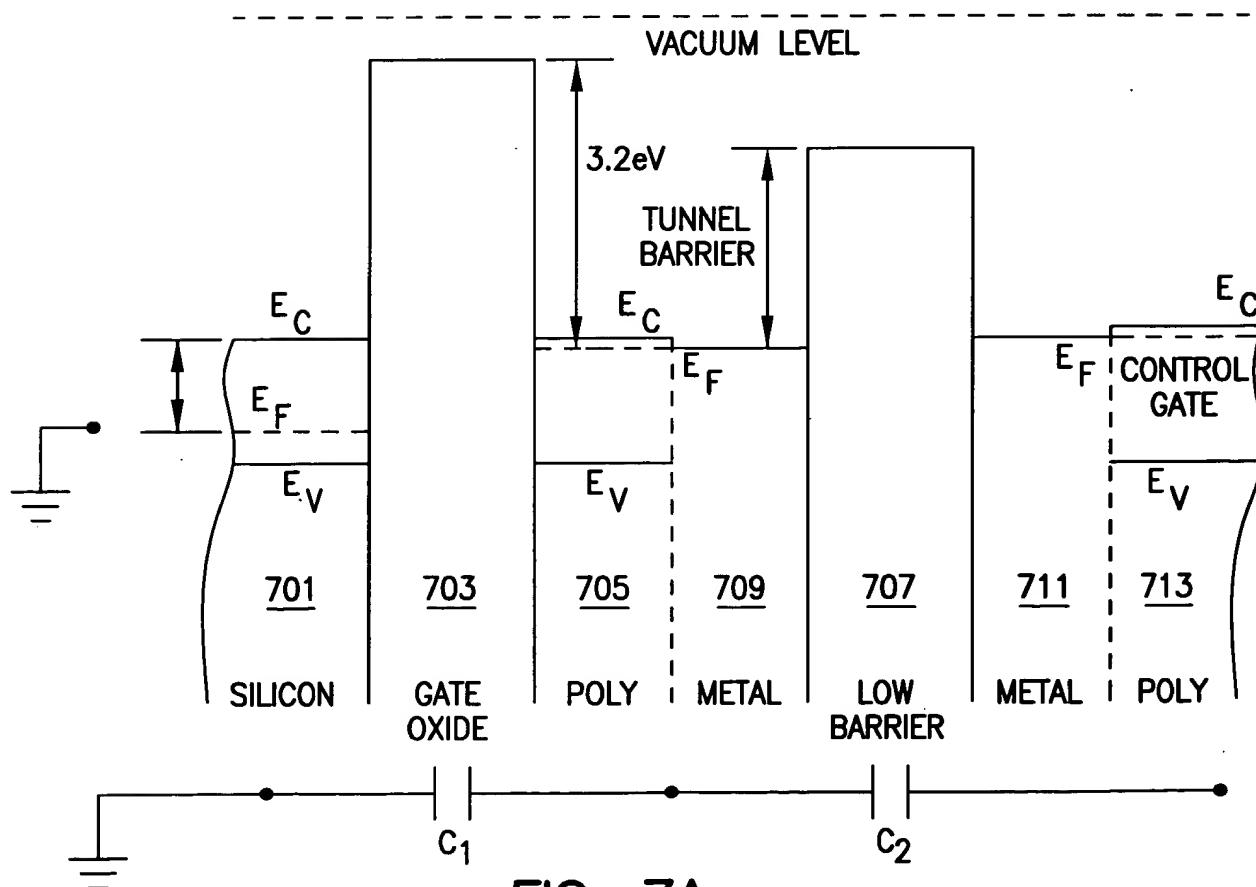
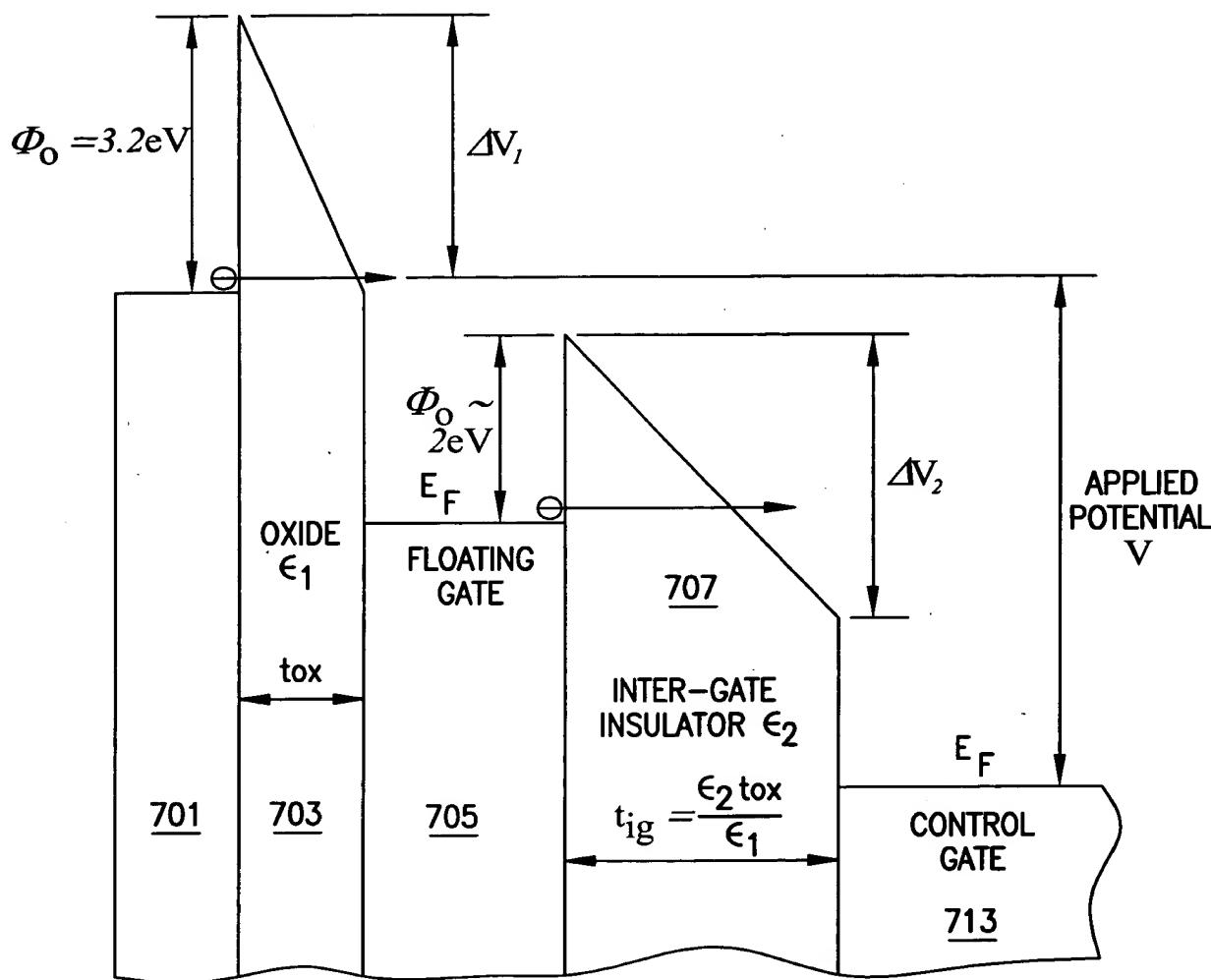


FIG. 7A

13/17



$$J = \frac{q^2 E^2 - E_0/E}{4\pi h \Phi} e \quad E_0 = \frac{8\pi}{3} \sqrt{\frac{2m^*}{h}} \Phi^{3/2}$$

FIG. 7B

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14/17

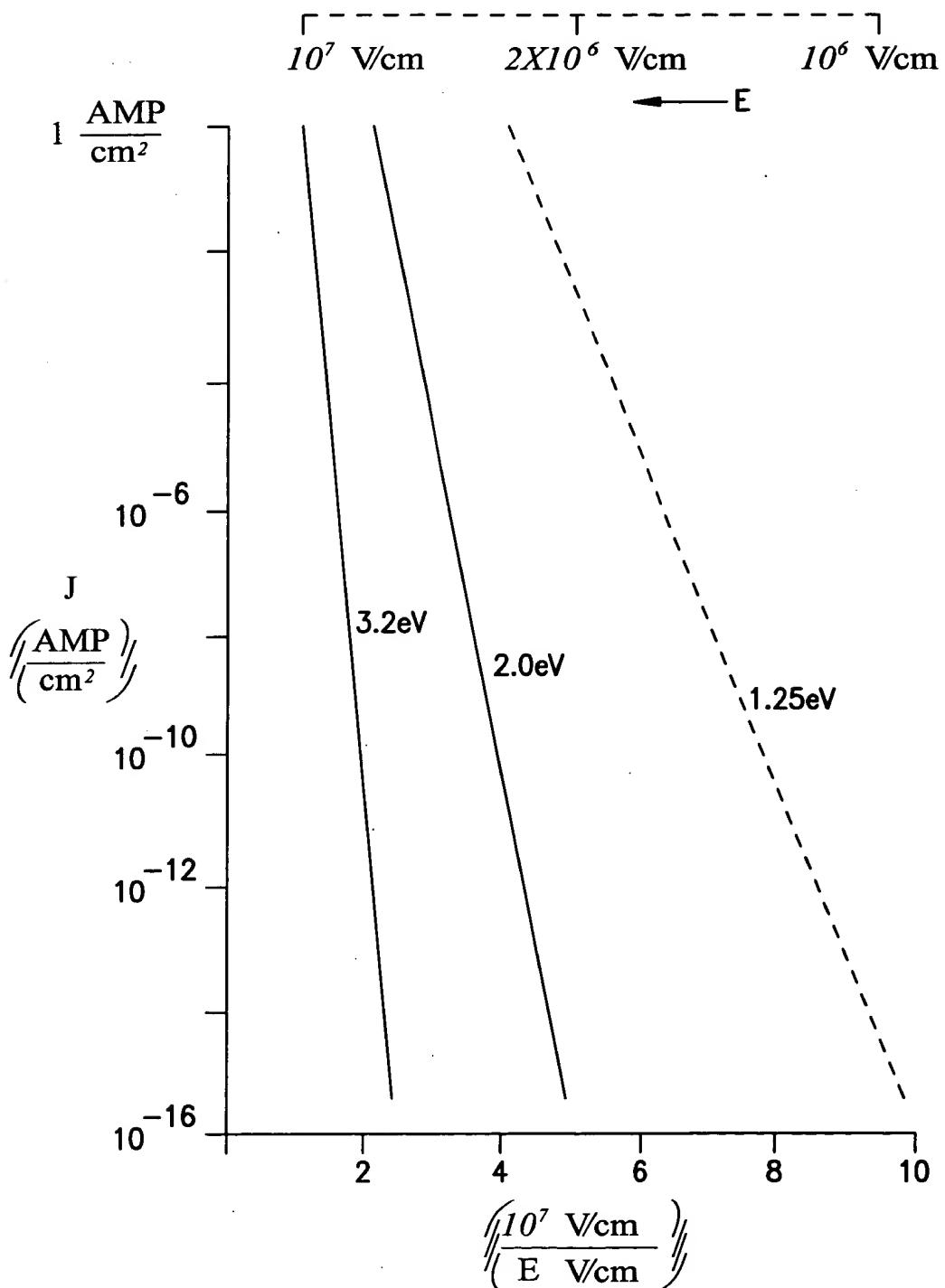


FIG. 7C

FIGURE 8 - 0000000000000000

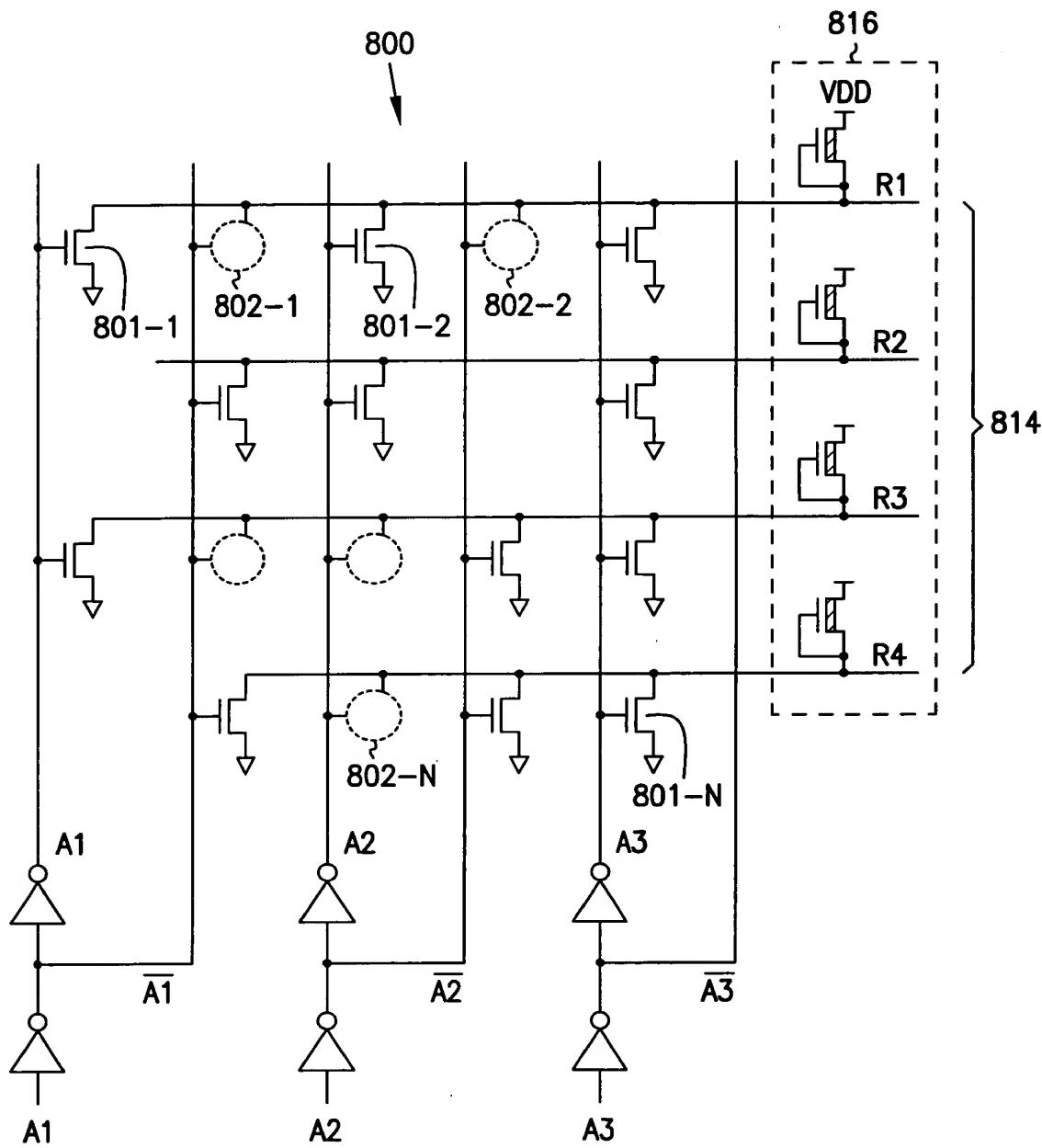


FIG. 8

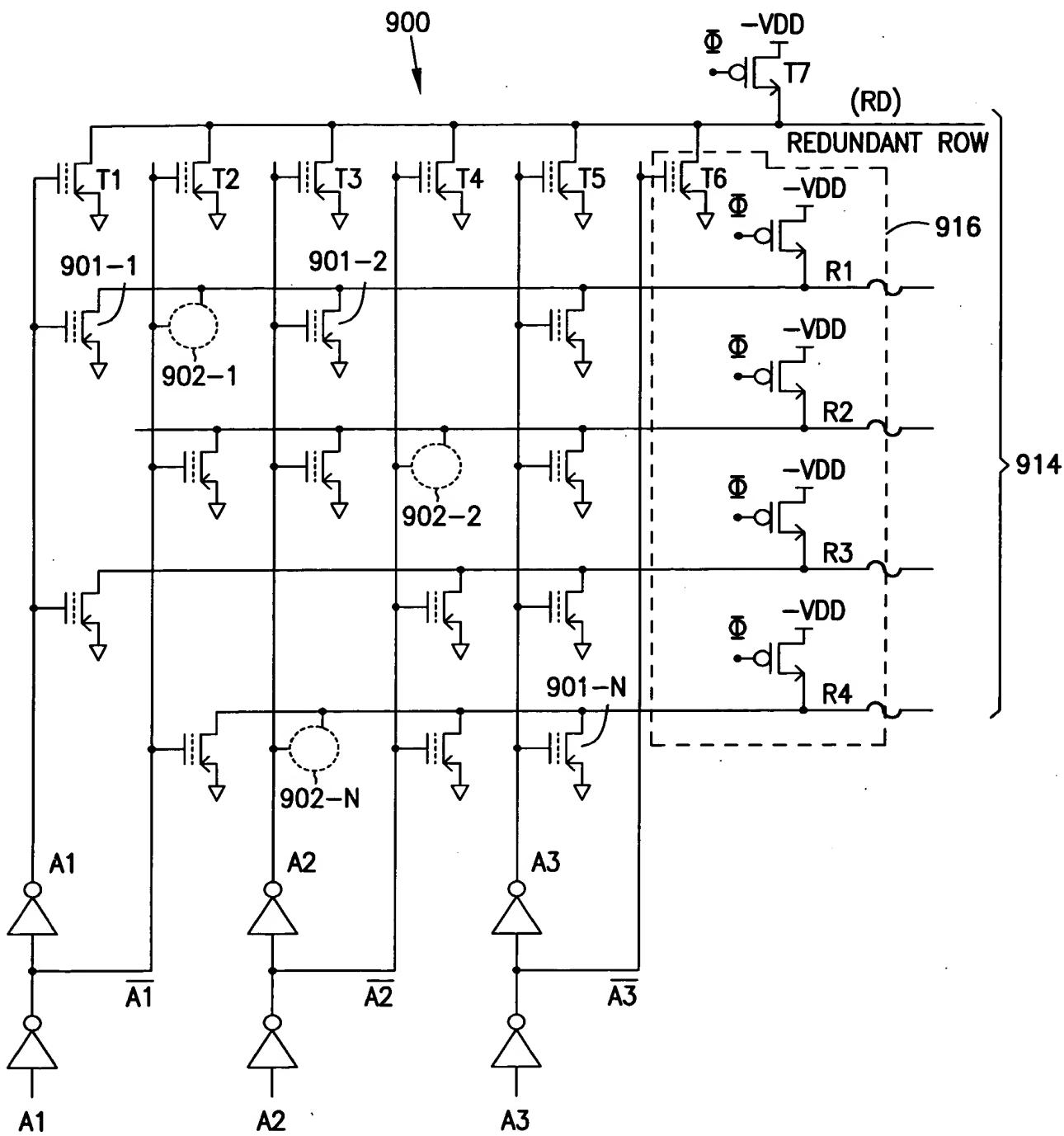


FIG. 9

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17/17

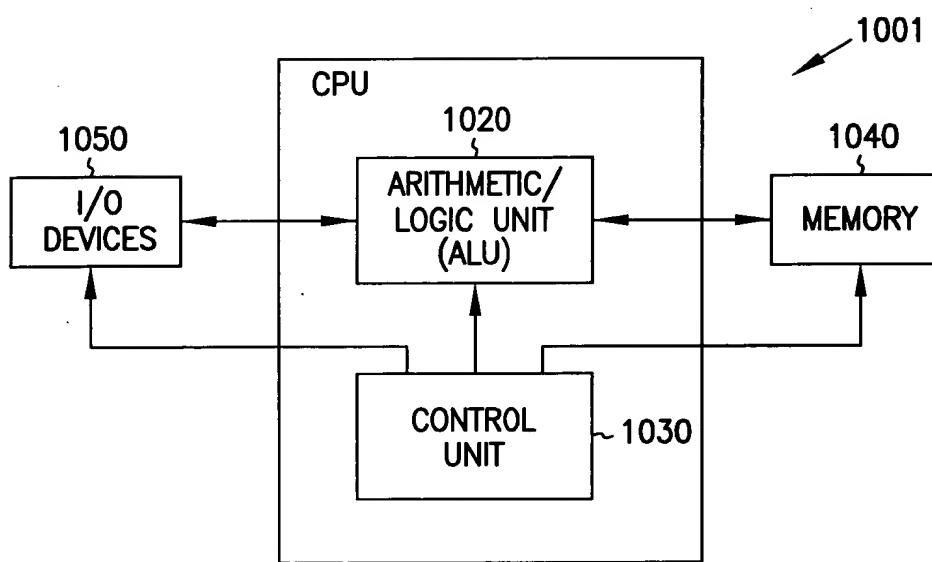


FIG. 10